

# NESG2031M05

## Data Sheet

NPN SiGe RF Transistor for Low Noise, High-Gain Amplification  
Flat-Lead 4-Pin Thin-Type Super Minimold (M05)

R09DS0035EJ0400  
Rev. 4.00  
Jun 20, 2012

### FEATURES

- The device is an ideal choice for low noise, high-gain at low current amplifications.  
NF = 0.8 dB TYP., Ga = 17.0 dB TYP. @ V<sub>CE</sub> = 2 V, I<sub>C</sub> = 5 mA, f = 2 GHz  
NF = 1.3 dB TYP., G<sub>a</sub> = 10.0 dB TYP. @ V<sub>CE</sub> = 2 V, I<sub>C</sub> = 5 mA, f = 5.2 GHz
- Maximum stable power gain: MSG = 21.5 dB TYP. @ V<sub>CE</sub> = 3 V, I<sub>C</sub> = 20 mA, f = 2 GHz
- High breakdown voltage technology for SiGe Tr. adopted: V<sub>CEO</sub> (absolute maximum ratings) = 5.0 V
- Flat-lead 4-pin thin-type super minimold (M05) package

<R> ORDERING INFORMATION

Part Number	Order Number	Package	Quantity	Supplying Form
NESG2031M05	NESG2031M05-A	Flat-lead 4-pin thin-type supper minimold (M05, 2012 PKG) (Pb-Free)	50 pcs (Non reel)	<ul style="list-style-type: none"> <li>8 mm wide embossed taping</li> <li>Pin 3 (Collector), Pin 4 (Emitter) face the perforation side of the tape</li> </ul>
NESG2031M05-T1	NESG2031M05-T1-A		3 kpcs/reel	

**Remark** To order evaluation samples, please contact your nearby sales office.

Unit sample quantity is 50 pcs.

### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = +25°C)

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V <sub>CBO</sub>	13.0	V
Collector to Emitter Voltage	V <sub>CEO</sub>	5.0	V
Emitter to Base Voltage	V <sub>EBO</sub>	1.5	V
Collector Current	I <sub>C</sub>	35	mA
Total Power Dissipation	P <sub>tot</sub> <sup>Note</sup>	175	mW
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-65 to +150	°C

Note: Mounted on 1.08 cm<sup>2</sup> × 1.0 mm (t) glass epoxy PCB

### CAUTION

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

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<R> ELECTRICAL CHARACTERISTICS ( $T_A = +25^\circ C$ )

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Collector Cut-off Current	$I_{CBO}$	$V_{CB} = 5 V, I_E = 0$	—	—	100	nA
Emitter Cut-off Current	$I_{EBO}$	$V_{EB} = 1 V, I_C = 0$	—	—	100	nA
DC Current Gain	$h_{FE}$ <sup>Note 1</sup>	$V_{CE} = 2 V, I_C = 5 mA$	130	190	260	—
RF Characteristics						
Gain Bandwidth Product	$f_T$	$V_{CE} = 3 V, I_C = 20 mA, f = 2 GHz$	20	25	—	GHz
Insertion Power Gain	$S_{21e}$ <sup>2</sup>	$V_{CE} = 3 V, I_C = 20 mA, f = 2 GHz$	16.0	18.0	—	dB
Noise Figure (1)	NF	$V_{CE} = 2 V, I_C = 5 mA, f = 2 GHz, Z_S = Z_{Sopt}, Z_L = Z_{Lopt}$	—	0.8	1.1	dB
Noise Figure (2)	NF	$V_{CE} = 2 V, I_C = 5 mA, f = 5.2 GHz, Z_S = Z_{Sopt}, Z_L = Z_{Lopt}$	—	1.3	—	dB
Associated Gain (1)	$G_a$	$V_{CE} = 2 V, I_C = 5 mA, f = 2 GHz, Z_S = Z_{Sopt}, Z_L = Z_{Lopt}$	15.0	17.0	—	dB
Associated Gain (2)	$G_a$	$V_{CE} = 2 V, I_C = 5 mA, f = 5.2 GHz, Z_S = Z_{Sopt}, Z_L = Z_{Lopt}$	—	10.0	—	dB
Reverse Transfer Capacitance	$C_{re}$ <sup>Note 2</sup>	$V_{CB} = 2 V, I_E = 0, f = 1 MHz$	—	0.15	0.25	pF
Maximum Stable Power Gain	MSG <sup>Note 3</sup>	$V_{CE} = 3 V, I_C = 20 mA, f = 2 GHz$	19.0	21.5	—	dB
Gain 1 dB Compression Output Power	$P_O(1 \text{ dB})$	$V_{CE} = 3 V, I_C = 20 mA, f = 2 GHz, Z_S = Z_{Sopt}, Z_L = Z_{Lopt}$	—	13	—	dBm
3rd Order Intermodulation Distortion Output Intercept Point	OIP <sub>3</sub>	$V_{CE} = 3 V, I_C = 20 mA, f = 2 GHz, Z_S = Z_{Sopt}, Z_L = Z_{Lopt}$	—	23	—	dBm

Notes: 1. Pulse measurement: PW ≤ 350 μs, Duty Cycle ≤ 2%

2. Collector to base capacitance when the emitter grounded

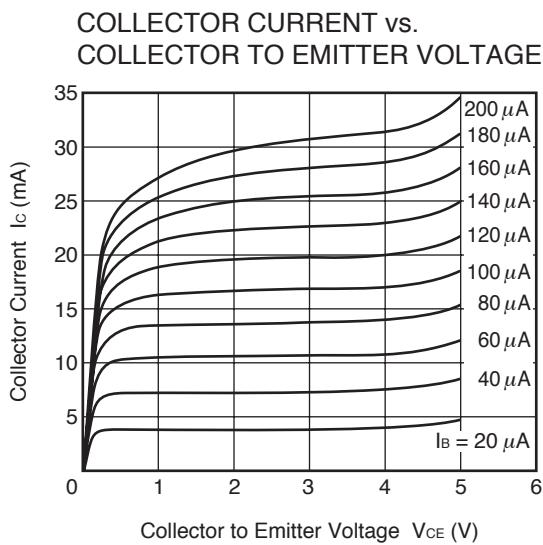
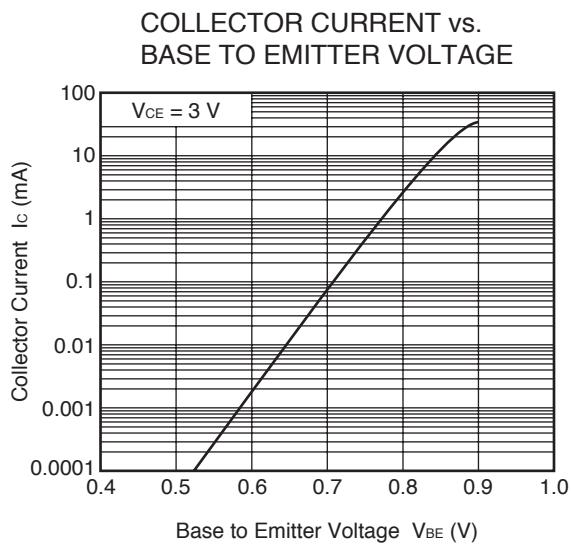
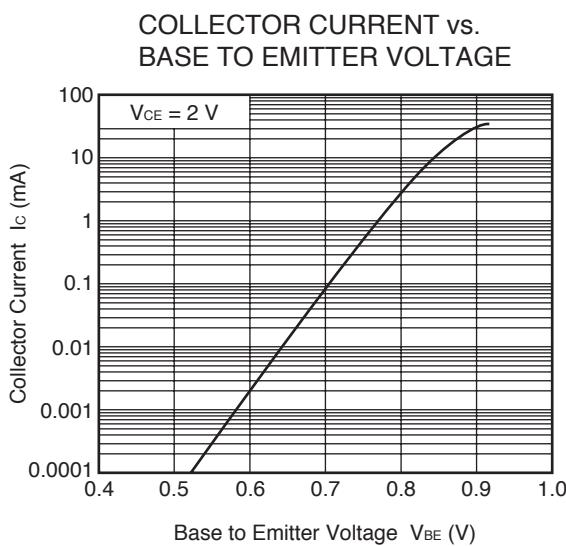
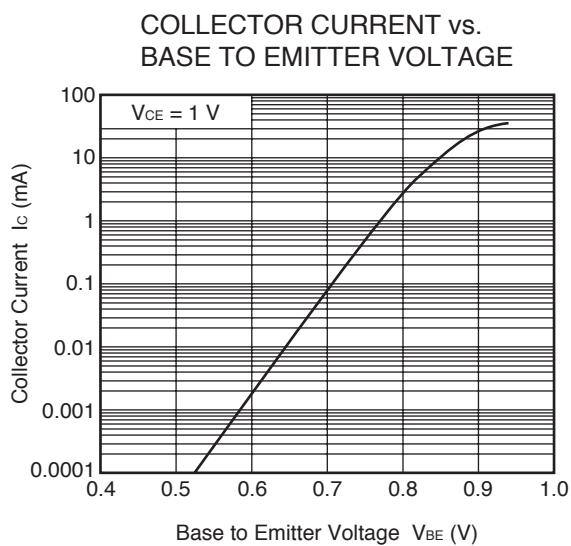
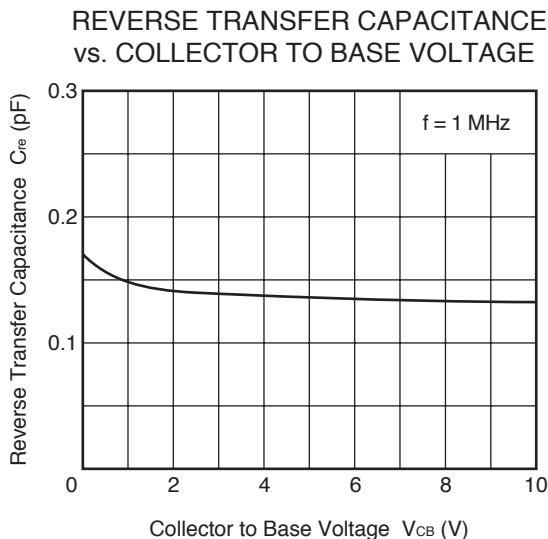
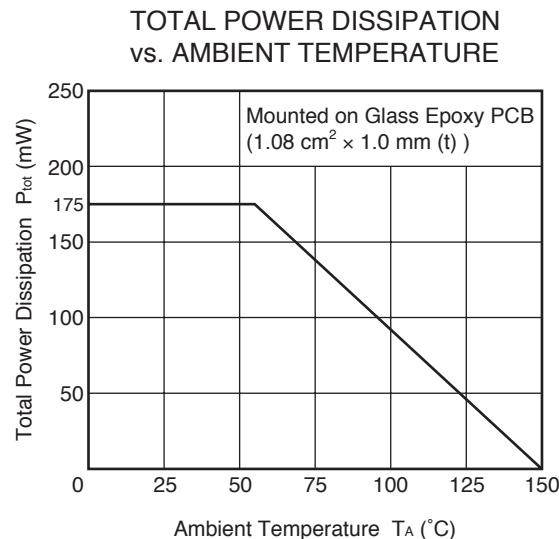
$$\text{MSG} = \left| \frac{S_{21}}{S_{12}} \right|$$

 **$h_{FE}$  CLASSIFICATION**

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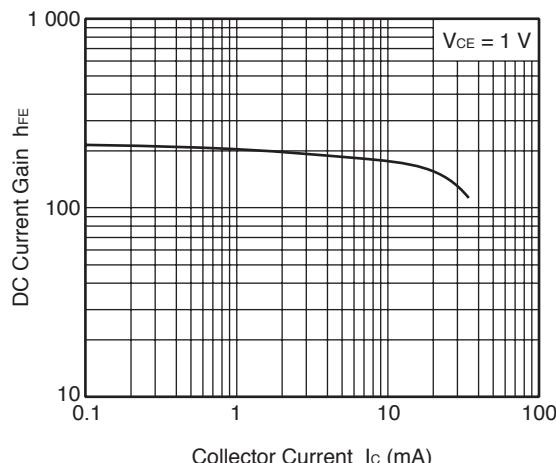
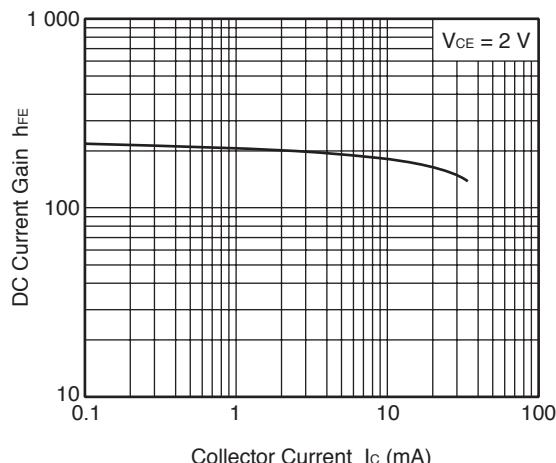
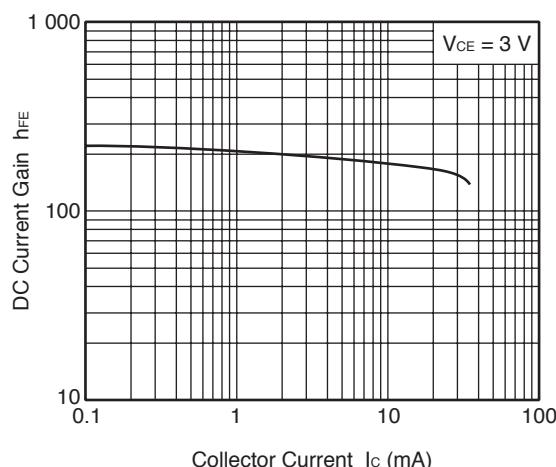
Rank	FB/YFB
Marking	T1H
$h_{FE}$ Value	130 to 260

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**TYPICAL CHARACTERISTICS ( $T_A = +25^\circ\text{C}$ , unless otherwise specified)**

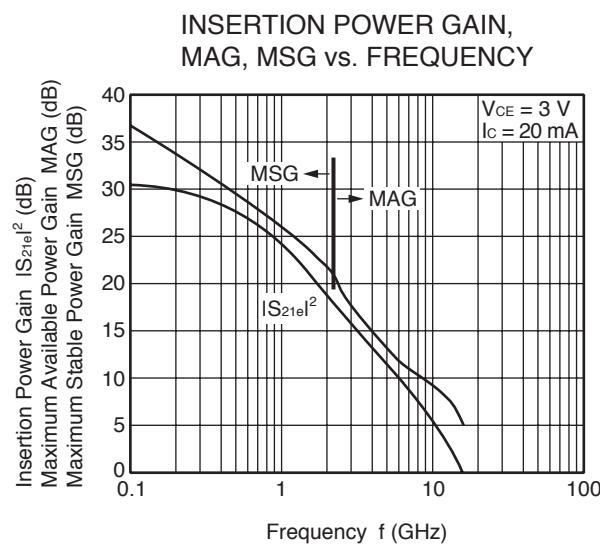
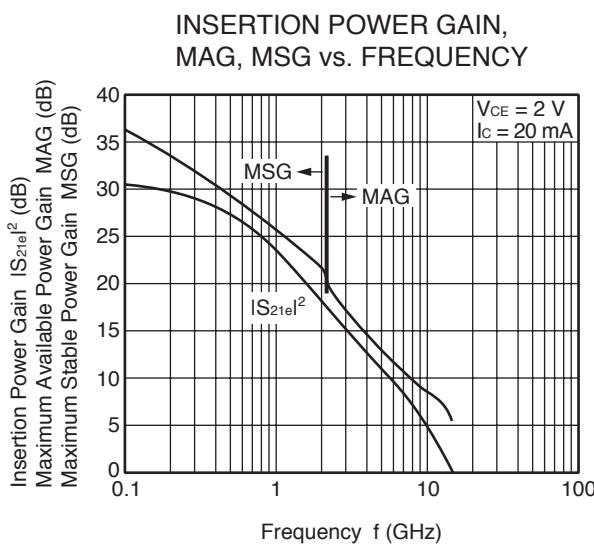
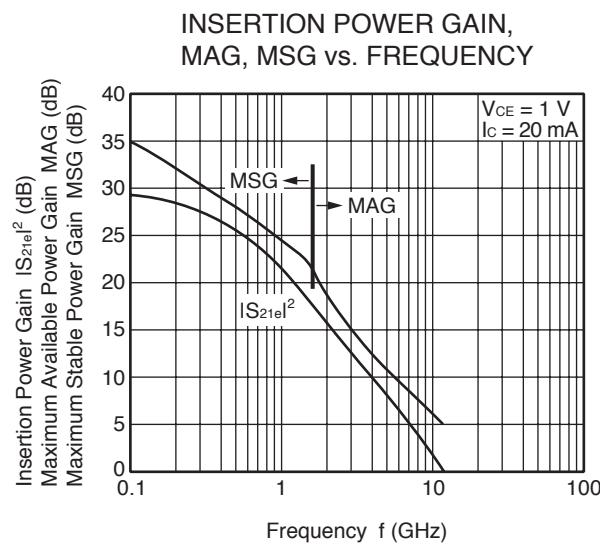
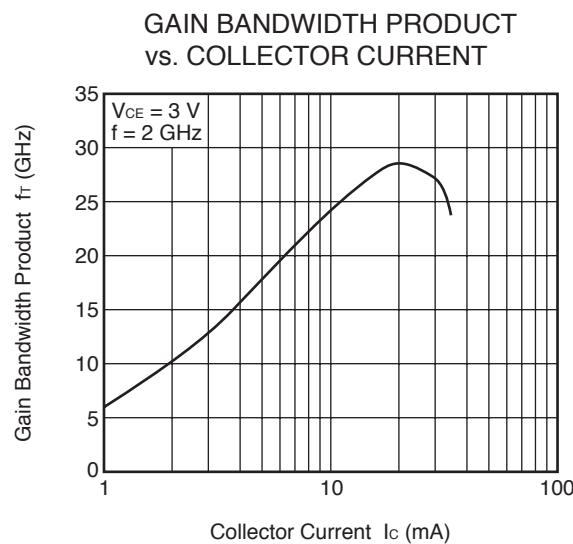
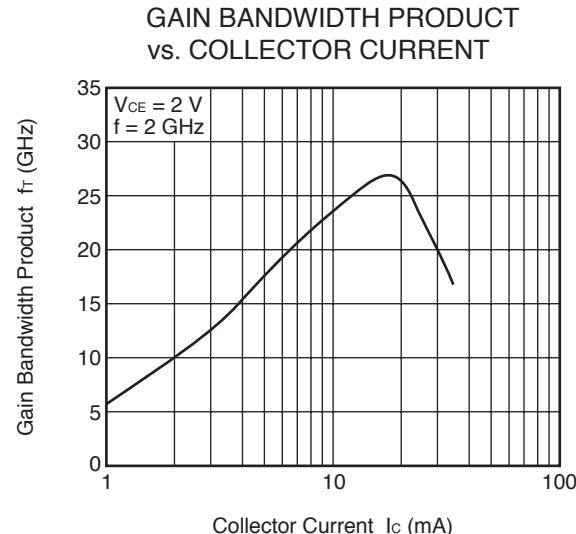
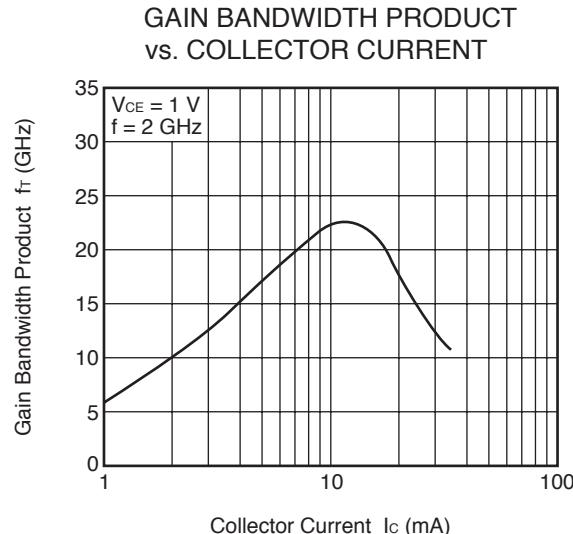
**Remark** The graph indicates nominal characteristics.

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DC CURRENT GAIN vs.  
COLLECTOR CURRENTDC CURRENT GAIN vs.  
COLLECTOR CURRENTDC CURRENT GAIN vs.  
COLLECTOR CURRENT

**Remark** The graph indicates nominal characteristics.

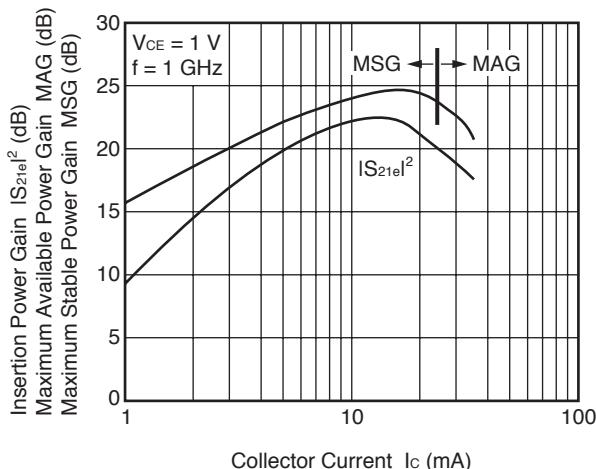
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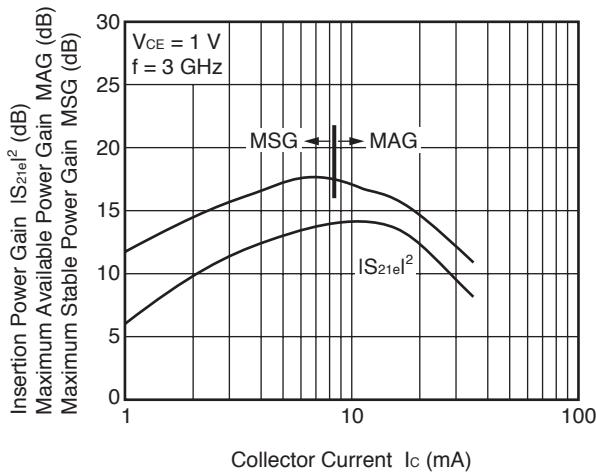
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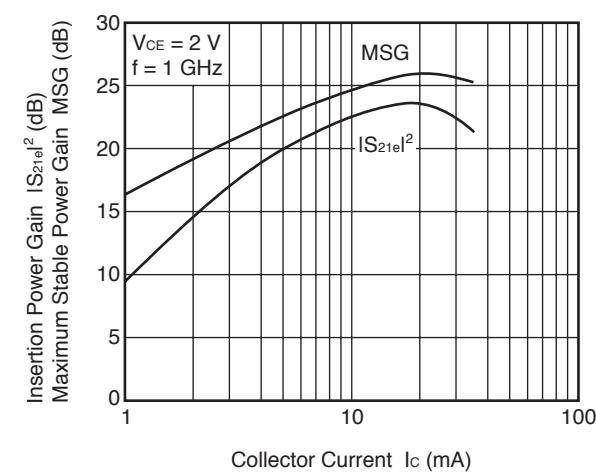
INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT



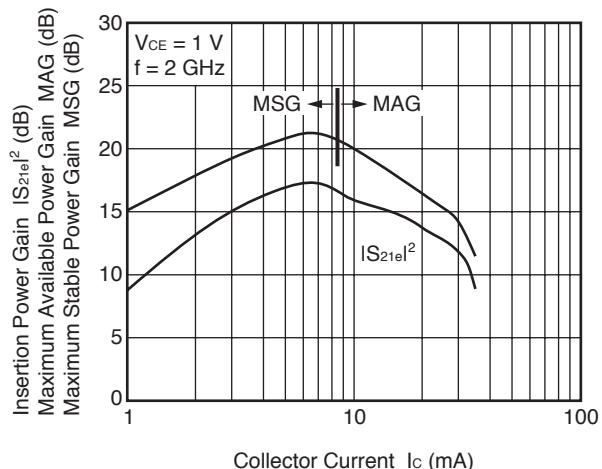
INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT



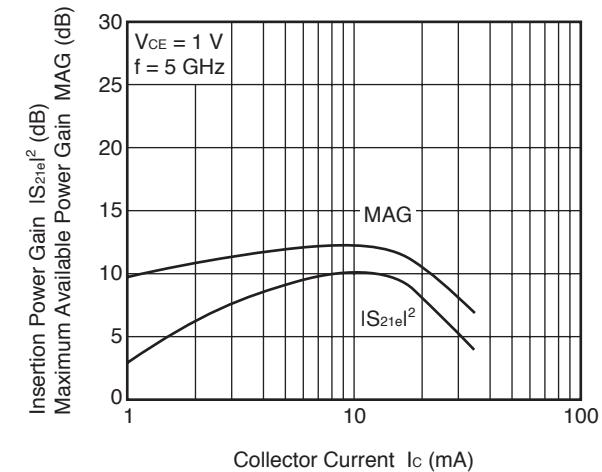
INSERTION POWER GAIN, MSG vs. COLLECTOR CURRENT



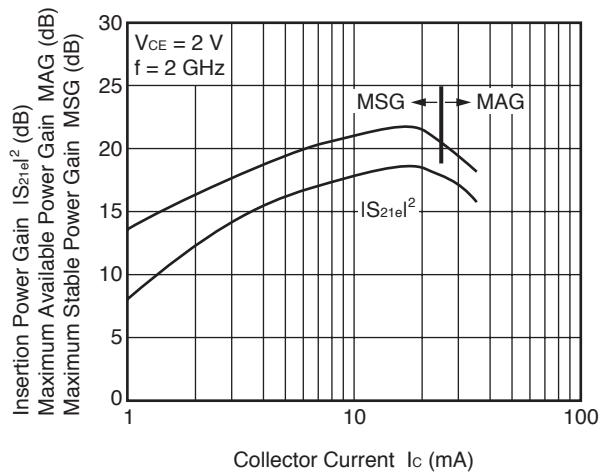
INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT



INSERTION POWER GAIN, MAG vs. COLLECTOR CURRENT

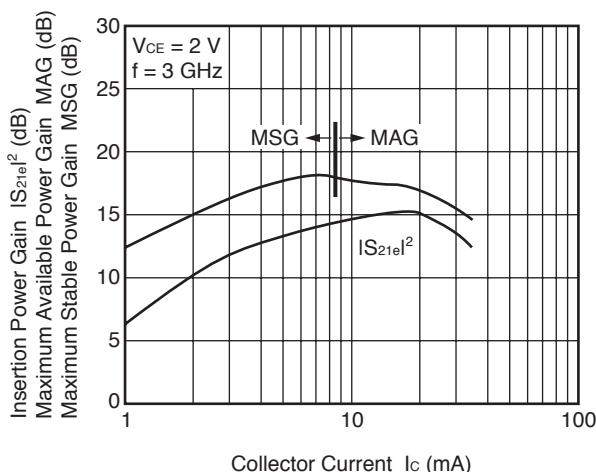
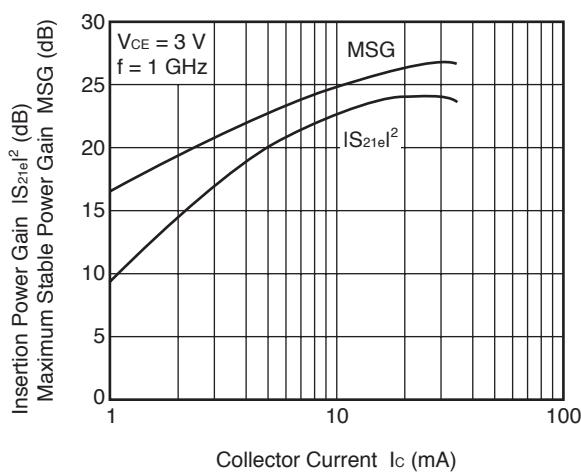
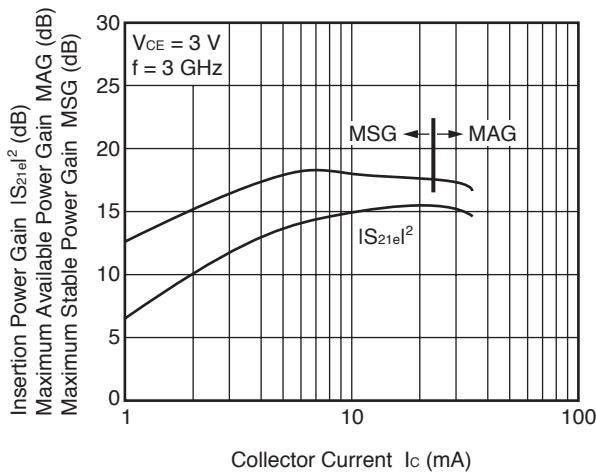
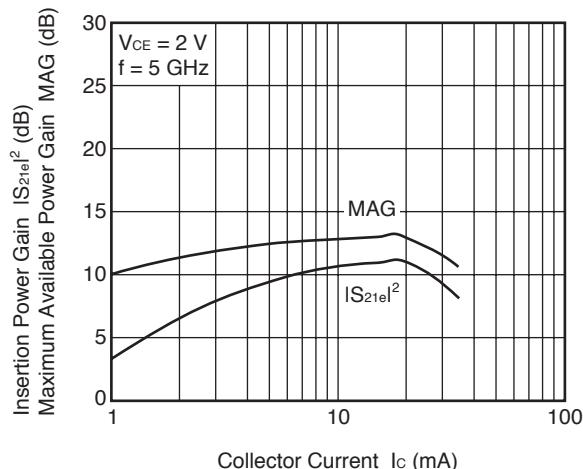
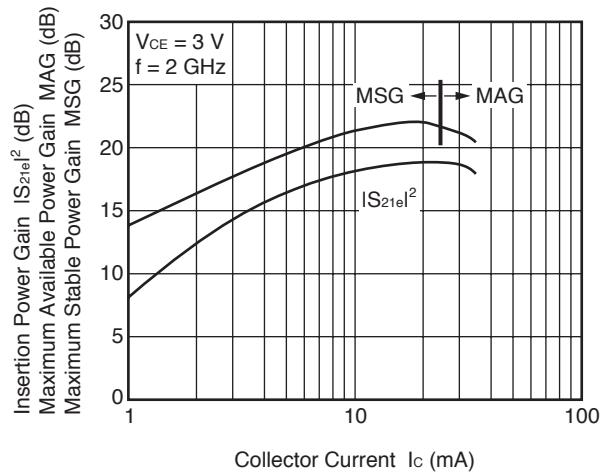
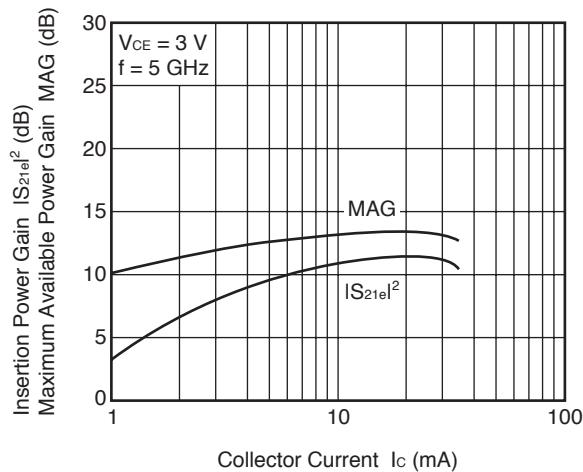


INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT

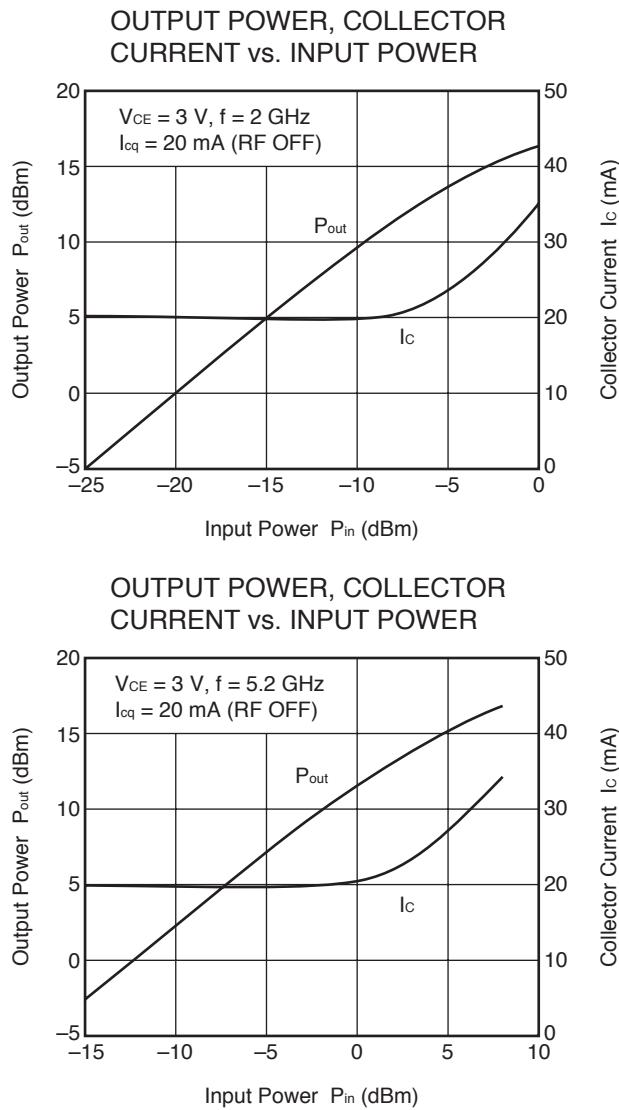
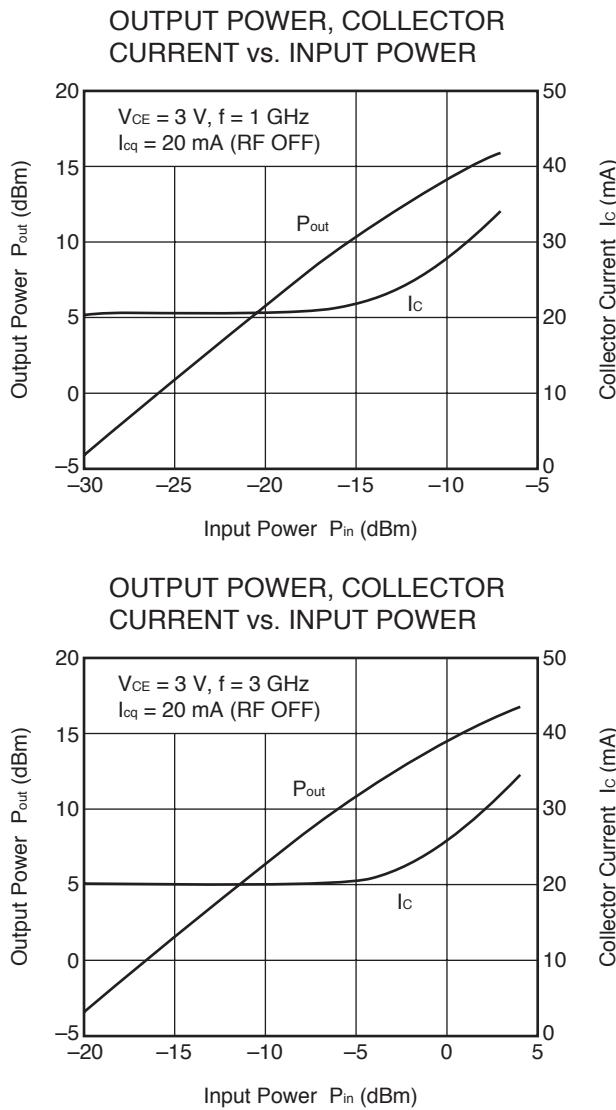


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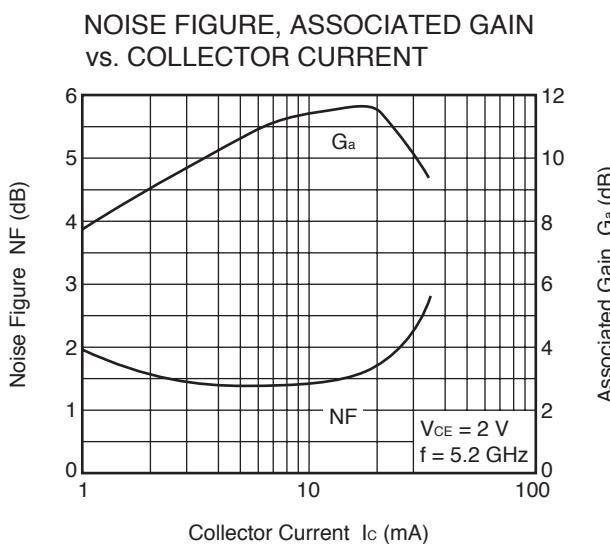
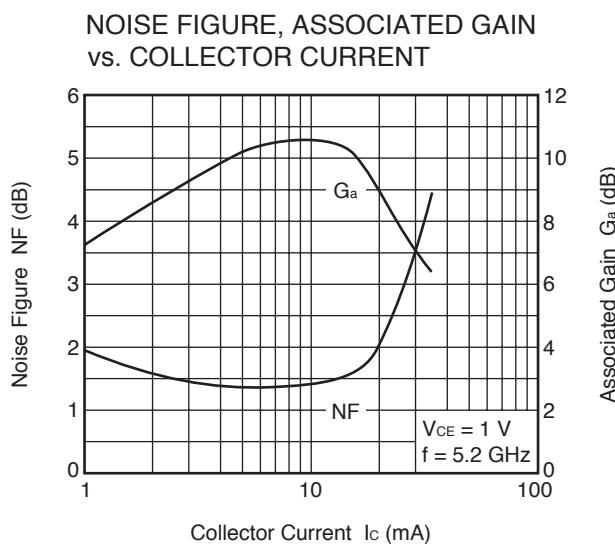
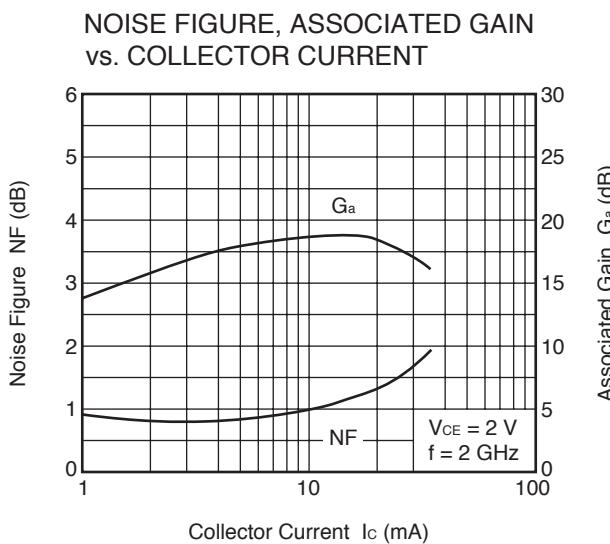
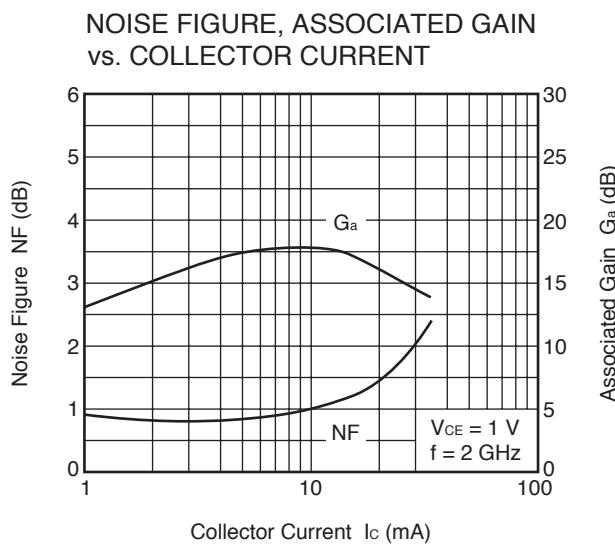
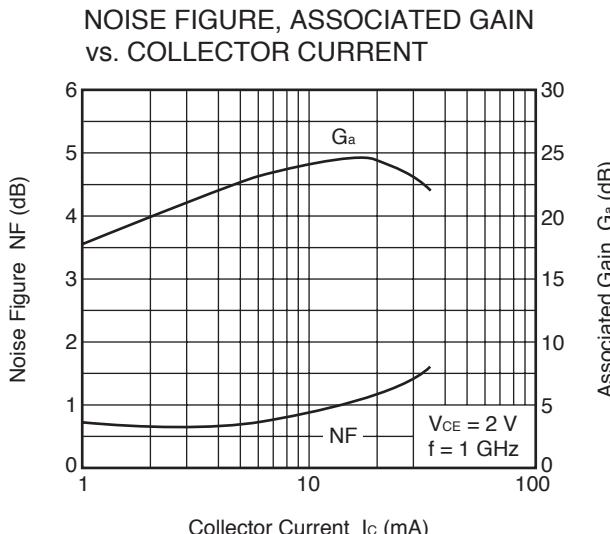
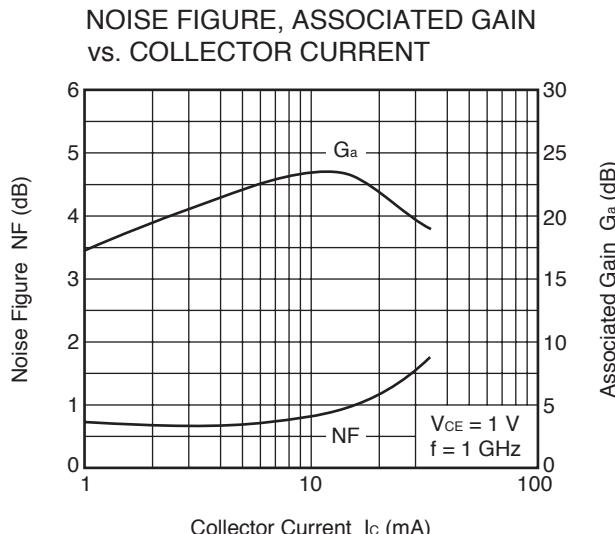
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INSERTION POWER GAIN, MAG, MSG  
vs. COLLECTOR CURRENTINSERTION POWER GAIN, MSG  
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vs. COLLECTOR CURRENT**Remark** The graph indicates nominal characteristics.

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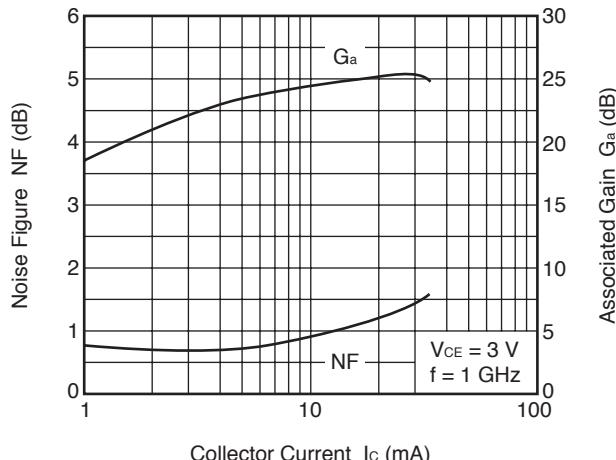


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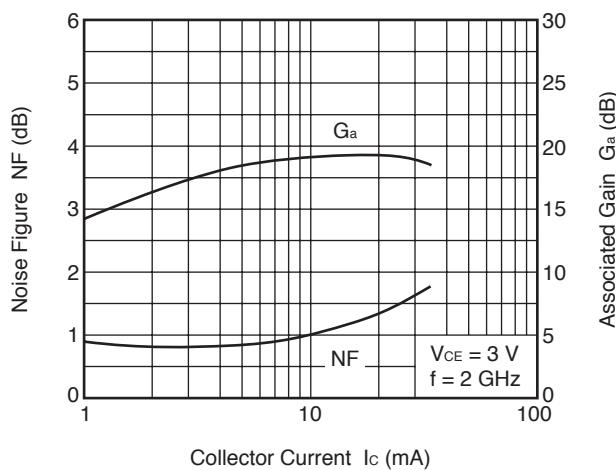


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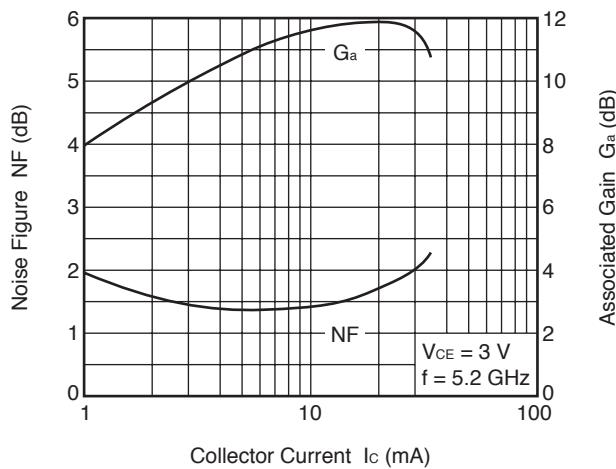
**NOISE FIGURE, ASSOCIATED GAIN  
vs. COLLECTOR CURRENT**



**NOISE FIGURE, ASSOCIATED GAIN  
vs. COLLECTOR CURRENT**



**NOISE FIGURE, ASSOCIATED GAIN  
vs. COLLECTOR CURRENT**



**Remark** The graphs indicate nominal characteristics.

## S-PARAMETERS

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S-parameters and noise parameters are provided on our web site in a form (S2P) that enables direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.

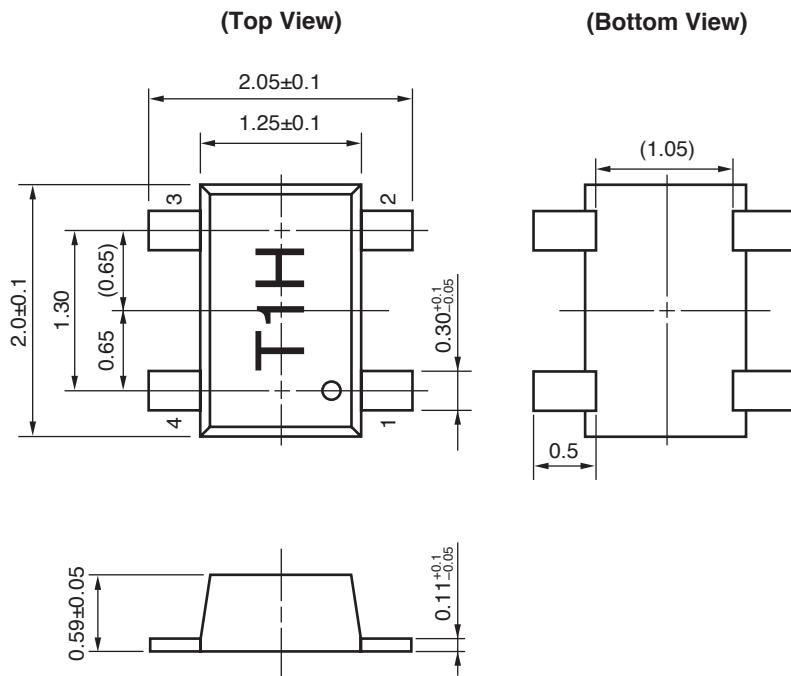
Click here to download S-parameters.

[Products] → [RF Devices] → [Device Parameters]

URL <http://www.renesas.com/products/microwave/>

## PACKAGE DIMENSIONS

<R> FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M05, 2012 PKG) (UNIT: mm)



### PIN CONNECTION

1. Base
2. Emitter
3. Collector
4. Emitter

**Remark ( ) :** Reference value

Revision History		NESG2031M05 Data Sheet	
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Rev.	Date	Description	
		Page	Summary
-	Sep 2004	-	Previous No. : PU10189EJ03V0DS
4.00	Jun 20, 2012	p.1	Modification of <b>ORDERING INFORMATION</b>
		p.2	Modification of <b>ELECTRICAL CHARACTERISTICS</b> Modification of <b><math>h_{FE}</math> CLASSIFICATION</b>
		p.11	Modification of <b>S-PARAMETERS</b>
		p.12	Modification of <b>PACKAGE DIMENSIONS</b>

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