

Molding Type Module IGBT, 2 in 1 Package, 1200 V and 100 A



PRODUCT SUMMARY					
V _{CES}	1200 V				
I _C at T _C = 80 °C	100 A				
$V_{CE(on)}$ (typical) at $I_C = 100$ A, 25 °C	3.10 V				
Speed	8 kHz to 30 kHz				
Package	Double INT-A-PAK				
Circuit	Half bridge				

FEATURES

- NPT IGBT technology
- 10 µs short circuit capability
- · Low switching losses
- · Rugged with ultrafast performance
- V_{CE(on)} with positive temperature coefficient
- Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- · Switching mode power supplies
- · Inductive heating
- Electronic welder

DESCRIPTION

Vishay's IGBT power module provides ultrafast switching speed as well as short circuit ruggedness. It is designed for applications such as electronic welders and inductive heating.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 20	V	
Collector current		T _C = 25 °C	200		
Collector current	Ic	T _C = 80 °C	100		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	200	А	
Diode continuous forward current	I _F		100		
Diode maximum forward current	I _{FM} ⁽¹⁾		200		
Maximum power dissipation	P _D	T _J = 150 °C	1136	W	
Isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.





IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Calla stanta ansittani alla	V _{CF(on)}	$V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25 ^{\circ}\text{C}$	-	3.10	3.60	V 00
Collector to emitter voltage		V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	3.45	-	
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_{C} = 1$ mA, $T_{J} = 25$ °C	4.4	4.9	6.0	
Zero gate voltage collector current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_J = 25$ °C	-	=.	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_J = 25$ °C	-	-	400	nA

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	300	-	- ns
Rise time	t _r		-	64	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{q} = 5.6 \Omega,$	-	340	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, L = 200 \text{ nH}, T_{J} = 25 \text{ °C}$	-	105	-	
Turn-on switching loss	E _{on}]	-	4.76	-	- mJ
Turn-off switching loss	E _{off}]	-	4.25	-	
Turn-on delay time	t _{d(on)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 5.6 \Omega,$ $V_{GE} = \pm 15 \text{ V}, L = 200 \text{ nH}, T_{J} = 125 ^{\circ}\text{C}$	-	320	-	- ns
Rise time	t _r		-	65	-	
Turn-off delay time	t _{d(off)}		-	350	-	
Fall time	t _f		-	132	-	
Turn-on switching loss	E _{on}		-	7.20	-	1
Turn-off switching loss	E _{off}		-	5.50	-	mJ
Short circuit withstand time	t _{SC}	T _J = 125 °C	-	-	10	μs
Input capacitance	C _{ies}		-	8.45	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 20 V, f = 1.0 MHz	-	0.76	-	nF
Reverse transfer capacitance	C _{res}		-	0.31	-	
SC data	I _{SC}	$t_{p} \leq 10~\mu s, V_{GE} = \pm~15~V, V_{CC} = 600~V, \\ V_{CEM} \leq 1200~V, T_{J} = 25~^{\circ}C$	-	900	-	
Internal gate resistance	R _{GINT}		-	2.4	-	Ω
Stray inductance	L _{CE}		-	-	18	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}		-	0.32	-	mΩ



DIODE ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Diada fanuard valtaga	V _F I _F	I _F = 100 A	T _C = 25 °C	-	1.82	2.22	V	
Diode forward voltage			T _C = 125 °C	-	1.95	-		
Diode reverse recovery charge	Q _{rr}	n	T _C = 25 °C	-	5.4	-	μC	
blode reverse recovery charge			T _C = 125 °C	-	11.2	-	μΟ	
Diada paak rayaraa raaayary ayrrant	1	$I_F = 100 \text{ A}, V_R = 600 \text{ V},$	T _C = 25 °C	-	81	-	Α	
Diode peak reverse recovery current	Irr	$dI_F/dt = -1900 A/\mu s,$ $V_{GE} = -15 V$	T _C = 125 °C	-	101	-		
Diede reverse recovery energy	E _{rec}	E _{rec}		T _C = 25 °C	-	3.54	-	ma I
Diode reverse recovery energy				T _C = 125 °C	-	6.57		mJ

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	e T _J		-40	-	150	°C
Storage temperature range	T _{Stg}		-40	-	125	
Junction to case	R _{thJC}		-	-	0.141	
Diod	e nthJC		-	-	0.225	°C/W
Case to sink	R _{thCS}	Conductive grease applied	-	0.035	-	
Mounting torque		Power terminal screw: M6 2.5 to 5		2.5 to 5.0) Nm	
		Mounting screw: M6	3.0 to 6.0		INIII	
Weight				300		g

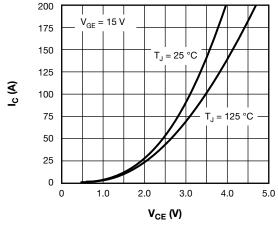


Fig. 1 - IGBT Typical Output Characteristics

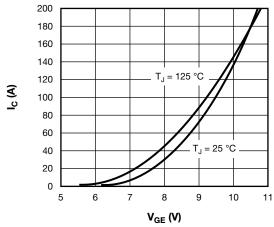


Fig. 2 - IGBT Typical Transfer Characteristics





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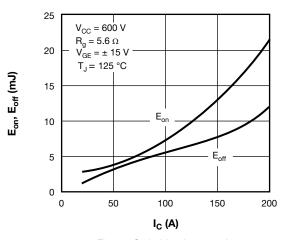


Fig. 3 - Switching Loss vs. I_{C}

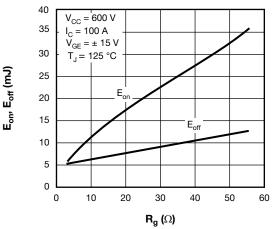


Fig. 4 - IGBT Switching Loss vs. Rq

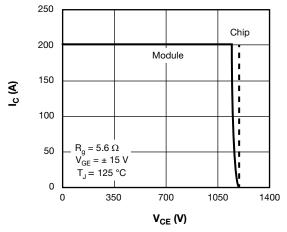


Fig. 5 - RBSOA

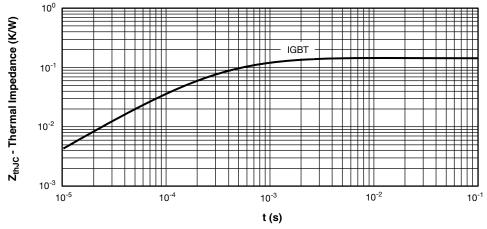


Fig. 6 - IGBT Transient Thermal Impedance



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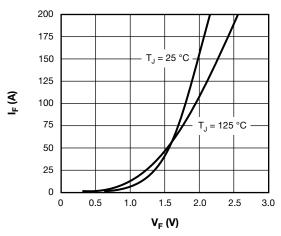


Fig. 7 - Diode Typical Forward Characteristics

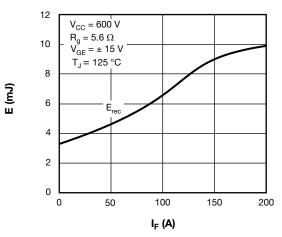


Fig. 8 - Diode Switching Loss vs. IF

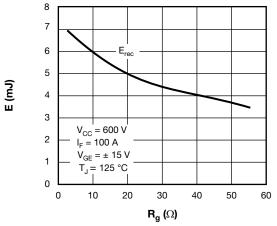


Fig. 9 - Diode Switching Loss vs. Rq

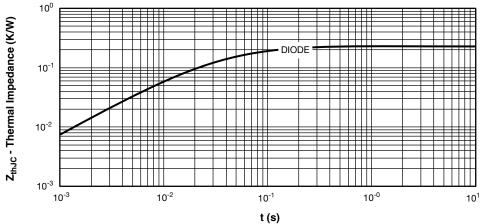
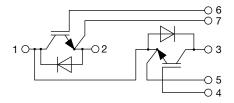


Fig. 10 - Diode Transient Thermal Impedance



CIRCUIT CONFIGURATION

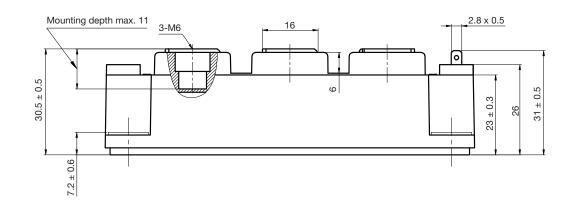


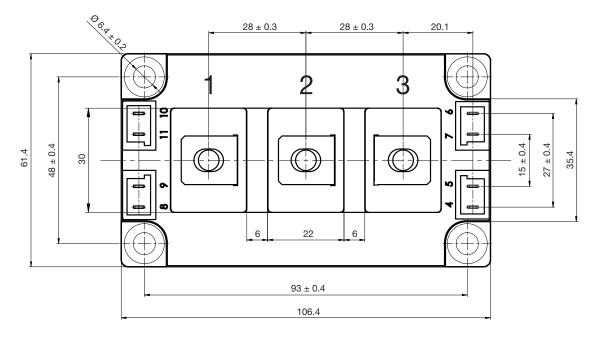
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95525



Double INT-A-PAK

DIMENSIONS in millimeters (inches)







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