

# AN4782 Application note

Getting started with SPC56B-Discovery board

### Introduction

The SPC560B-Discovery board, hereafter described as SPC560B-DIS, is an evaluation tool supporting STMicroelectronics SPC56B-Line microcontrollers. The selected solution is the quickest way to discover the SPC56B family, to program the device, to build and to debug applications.

SPC560B-DIS Discovery board is based on the 32-bit microcontrollers Power Architecture<sup>®</sup> Core SPC560B54L5.



#### Figure 1. SPC560B-DIS Discovery board with SPC560B54L5

The evaluation board allows full access to all of the CPUs I/O on the two 2x36 headers with exception of JTAG, TCK, TMS, TDI, TDO, XTAL, EXTAL and VDD\_LV pins.

SPC560B-DIS discovery board is supported by a specific application project inside SPC5Studio (a visual integrated software development environment to easily develop software for SPC56 MCU's), where micro start-up routines, I/O mapping and a simple test codes have been already designed by STMicroelectronics experts.

SPC5Studio is available for download on <u>www.st.com</u>.

SPC5Studio comes with HighTec GNU "C" compiler free fully featured 30 days trial version. An E2E Community is available on ST WEB.

The PCB, all the components and HW parts meet requirements of the applicable RoHS directives.

DocID028560 Rev 1

## Contents

| 1        | Gett  | ting started                                     |
|----------|-------|--|
|          | 1.1   | Delivery checklist                               |
|          | 1.2   | Setting up hardware and connections              |
| 2        | Ном   | <pre>v to connect and program SPC560B-DIS6</pre> |
|          | 2.1   | How to connect the board6                        |
|          | 2.2   | Load a test program                              |
| Appendix | κA    | General handling precautions 10                  |
| Appendix | ĸВ    | Reference documents                              |
| Revision | histo | ory  |



# List of tables

| Toble 1  | Desument revision history | 10     |
|----------|---------------------------|--------|
| Table T. | Document revision history | <br>12 |



# List of figures

| Figure 1. | SPC560B-DIS Discovery board with SPC560B54L5 1                          |
|-----------|---|
| Figure 2. | SPC5Studio - Project explorer - "Test App SPC560B-DIS discovery board"7 |
| Figure 3. | SPC5Studio - Project explorer – UDE folder and "debug.wsx"7             |
| Figure 4. | UDE Visual Platform – Window "Load binary target file"                  |
| Figure 5. | UDE Visual Platform - UDE - FLASH/OTP Memory Programming tool           |
| Figure 6. | UDE Visual Platform - UDE - FLASH/OTP Memory Programming tool           |



## **1** Getting started

#### 1.1 Delivery checklist

The SPC560B-DIS kit contains the following items:

- #1 SPC560B-DIS evaluation board; the label applied on the PCB bottom side of the PCB is the debugger serial number.
- #1 USB type A to mini-B cable.

The jumpers allow configuring the board to enable features and different flash modalities.

### 1.2 Setting up hardware and connections

To setup the hardware follow this procedure:

- 1. Remove the board and the PSU from the protective plastic envelops.
- 2. Connect the USB cable to the board then in a PC USB port.
- 3. The board is supplied and ready to be used and programmed.
- 4. The LED D6 (green) reveals +5 V supply is present and the regulator works properly.

The user LEDs D7 and D8 (both green) are blinking and they show all connections are right and the microcontroller is working properly<sup>(a)</sup>.

a. The LEDs are blinking because a demonstration code has been stored in the microcontroller. The software is included in the "Test App SPC560B-DIS board"



## 2 How to connect and program SPC560B-DIS

The following procedure describes how to connect and flash the microcontroller using the USB port and the integrated debugger.

### 2.1 How to connect the board

The procedure describes how to connect the board and to program PLS USB-JTAG adapter.

- 1. Turn off the board (Switch S1="OFF")
- 2. Connect the USB cable to the board
- 3. Plug the USB cable to the PC (USB 2.0 or higher)
- 4. Turn on the board (Switch S1="ON")
- 5. The user LEDs D7 and D8 are blinking
- 6. The controller is ready to be programmed with the user software.

### 2.2 Load a test program

The procedure describes how to flash the microcontroller using SPC5Studio and UDE-PLS software<sup>(b)</sup>.

- 1. Run SPC5Studio
- 2. Import the test application developed for this board ("Test App OS-Less for SPC560B-DIS Discovery board.zip")
- 3. In "Project Explorer" select and open the test application project named "Test App OS-Less for SPC560B-DIS Discovery board".



b. Both of these softwares must be downloaded and installed in your PC.

| rigure 2. Or ocoluaio |   | пч |
|-----------------------|---|----|
| a 😂 SPC560            | 0B OS-Less Test Application for SPC560B-DIS Discovery board |    |
| > 🔑 con               | mponents  |    |
| 🔑 sou                 | urce  |    |
| > 🔁 bui               | uild  |    |
| D 🔁 UDI               | DE  |    |
| 📄 app                 | plication.ld  |    |
| 😽 con                 | onfiguration.xml  |    |
| .c mai                | ain.c   |    |
| 🗋 Ma                  | akefile   |    |
| 📄 read                | adme.txt  |    |
| 📄 use                 | er.mak  |    |

Figure 2. SPC5Studio - Project explorer – "Test App SPC560B-DIS discovery board"

- 4. The demo code is in the file "main.c<sup>(c)</sup>"
- 5. Save the project and build it.
- 6. Select and open the UDE folder then click to "debug.wsx": a new program (UDE Visual Platform) is launched in a new window.

| Figure | 3 SPC5   | Studio - | Project | explorer | folder | and | "dehua w | sy' |
|--------|----------|----------|---------|----------|--------|-----|----------|-----|
| Iguic  | 3. 01 05 | otuaio - | 110,000 | CAPIOICI | loidei | and | ucbug.w  | 37  |

| a ( | SPC560B OS-Less Test Application for SPC560B-DIS Discovery board |
|-----|--|
|     | Components   |
|     | 😕 source   |
|     | ▷ 🗁 build  |
|     | 🛛 🧁 UDE  |
|     | 🛤 debug .wsx   |
|     | stm_xpc560b64_minimodule_debug_jtag.cfg                          |
|     | application.ld   |
|     | 🕎 configuration.xml  |
|     | 🖻 main.c   |
|     | 🚡 Makefile   |
|     | 📄 readme.txt   |
|     | 📄 user.mak   |
|     |  |

- 7. In "Message View" a message informs if the board is connected and if the microcontroller has been identified by the programmer/debugger properly.
- 8. In the window "Load binary target file" should be present the path where the file "out.elf" has been generated. The default path is: "C:\SPC5Studio\workspace\SPC560B OS-Less Test Application for SPC560B-DIS Discovery board\build\". To select a different path, in the main bar select "File" then "Load Program" then select the path and the file to load.
- 9. Press "Load button" to load the binary "out.elf" file (*Figure 4*).

c. The code can be modified. The final result can be different with respect to the original code.



| Load binary target file                              |
|--|
| Do you want to load binary file for target           |
| Controller0.Core                                     |
| bation for SPC560B-DIS Discovery board\build\out.elf |
| from Workspace?                                      |
| Load Cancel  |
|  |

Figure 4. UDE Visual Platform – Window "Load binary target file"



#### Figure 5. UDE Visual Platform - UDE - FLASH/OTP Memory Programming tool

| PFLA: | iH: 1.5 MByte OnCh | nip FLASH (Starter | kit) |   | •           | 🔽 Enable   | Exit        |
|-------|--------------------|--------------------|------|---|-------------|------------|-------------|
| ,     |                    |                    |      |   |             |            |             |
| Inde: | Start              | End                | Size |   | Remove All  | Erase      | About       |
| 0     | 0x00000000         | 0x00007FFF         | 32K  |   |             |            |             |
|       | 0x00000000         | 0x0000203F         |      |   | Remove Sel. | Program    | Help        |
| 1     | 0x00008000         | 0x0000BFFF         | 16K  |   |             |            |             |
| 2     | 0x0000C000         | UXUUUUFFFF         | 16K  | = |             | Venty      | General     |
| 3     | UXUUU1UUUU         | 0x00017FFF         | 32K  |   | ave a       | 1575 1 1   |             |
| 4     | 0x00018000         | 0x0001FFFF         | 32N  |   | SW Protect  | HW Protect |             |
| 6     | 0x00020000         | 0x0005FFFF         | 12   |   |             |            |             |
| 7     | 0x00060000         | 0x0003FFFF         | 12   |   |             |            |             |
| 8     | 0x00080000         | 0x0009FFFF         | 12   |   |             |            | Program All |
| 9     | 0x000A0000         | 0x000BFFFF         | 12   |   |             |            |             |
| 10    | 0x000C0000         | 0x000DFFFF         | 12   | - | Info        | Setup      | Verify All  |

11. The execution of the flash procedure is reported the UDE Visual Platform - UDE - FLASH/OTP Memory Programming tool.



| Current FLASH/OTP Device :          | Current FLASH/0TP Device :          |
|-------------------------------------|-------------------------------------|
| 1.5 MByte OnChip FLASH (Starterkit) | 1.5 MByte OnChip FLASH (Starterkit) |
| Operation :                         | Operation :                         |
| Programming 00002000h - 00002077h   | Programming 00002000h - 00002077h   |
| Result :                            | Result :<br>success                 |
| Progress :                          | Progress :                          |
|                                     |                                     |

Figure 6. UDE Visual Platform - UDE - FLASH/OTP Memory Programming tool

- 12. Close the "UDE Visual Platform "
- 13. Remove and plug the USB cable.
- 14. Check the user LEDs D7 and D8: they are blinking according to the code developed and stored in the microcontroller

The document UM1602 (see Section Appendix B: Reference documents) is focused to describe and to understand how to use SPC5studio and how to create, build and debug a project.



## Appendix A General handling precautions

The following precautions are recommended when using the SPC560D-DIS board:

- Do not modify or manipulate the board when the DC supply or the USB cable is connected to the board.
- The connectors and cables should be plugged and removed when the board is off.
- Do not supply the board with a DC source higher than 12 V or with reverse polarity.
- Pay attention to the PSU DC power plug: it must be with 2.1 mm inner diameter and with positive connected to the center.
- It is recommended to use antistatic tools.



# Appendix B Reference documents

• SPC5Studio users guide (User manual UM1602, DocID024185).



# **Revision history**

| Date        | Revision | Changes          |
|-------------|----------|------------------|
| 09-Nov-2015 | 1        | Initial release. |

#### Table 1. Document revision history



#### IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics - All rights reserved



DocID028560 Rev 1